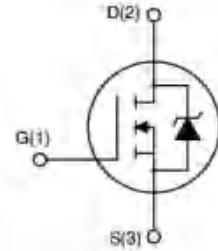


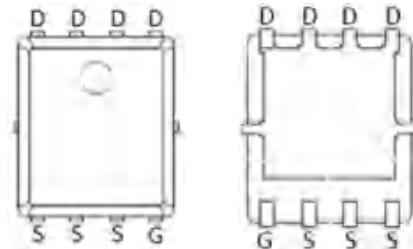
Feature

- 100V,85A
- $R_{DS(on)} < 4.6\text{m}\Omega @ V_{GS}=10\text{V}$ (TYP:3.8m Ω)
- $R_{DS(on)} < 6.4\text{m}\Omega @ V_{GS}=4.5\text{V}$ (TYP:5.2m Ω)
- Split Gate Trench Technology
- Lead free product is acquired
- Excellent $R_{DS(on)}$ and Low Gate Charge



Application

- PWM applications
- Load Switch
- Power management



PDFN5X6

Package Marking and Ordering Information

| Device Marking | Device | Device Package | Reel Size | Tape width | Quantity (PCS) |
|----------------|------------|----------------|-----------|------------|----------------|
| G046N01G | APG046N01G | PDFN5X6 | - | - | 5000 |

ABSOLUTE MAXIMUM RATINGS ($T_a=25^\circ\text{C}$ unless otherwise noted)

| Parameter | Symbol | Value | Unit |
|--|-----------|----------|---------------------------|
| Drain-Source Voltage | V_{DS} | 100 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Continuous Drain Current ($T_a = 25^\circ\text{C}$) | I_D | 85 | A |
| Continuous Drain Current ($T_a = 100^\circ\text{C}$) | I_D | 53 | A |
| Pulsed Drain Current ⁽¹⁾ | I_{DM} | 260 | A |
| Single Pulsed Avalanche Energy ⁽²⁾ | E_{AS} | 256 | mJ |
| Power Dissipation | P_D | 56.8 | W |
| Thermal Resistance from Junction to Case | R_{eJC} | 2.2 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance from Junction to Ambient | R_{eJA} | 50 | $^\circ\text{C}/\text{W}$ |
| Junction Temperature | T_J | 150 | $^\circ\text{C}$ |
| Storage Temperature | T_{STG} | -55~+150 | $^\circ\text{C}$ |

MOSFET ELECTRICAL CHARACTERISTICS($T_a=25^\circ C$ unless otherwise noted)

| Parameter | Symbol | Test Condition | Min | Type | Max | Unit |
|---|---------------|---|-----|------|-----------|-----------|
| Static Characteristics | | | | | | |
| Drain-source breakdown voltage | $V_{(BR)DSS}$ | $V_{GS} = 0V, I_D = 250\mu A$ | 100 | - | - | V |
| Zero gate voltage drain current | I_{DSS} | $V_{DS} = 100V, V_{GS} = 0V$ | - | - | 1 | μA |
| Gate-body leakage current | I_{GSS} | $V_{GS} = \pm 20V, V_{DS} = 0V$ | - | - | ± 100 | nA |
| Gate threshold voltage ⁽³⁾ | $V_{GS(th)}$ | $V_{DS} = V_{GS}, I_D = 250\mu A$ | 1.5 | 1.9 | 3.0 | V |
| Drain-source on-resistance ⁽³⁾ | $R_{DS(on)}$ | $V_{GS} = 10V, I_D = 30A$ | - | 3.8 | 4.6 | $m\Omega$ |
| | | $V_{GS} = 4.5V, I_D = 20A$ | - | 5.2 | 6.4 | $m\Omega$ |
| Gate Resistance | R_g | $V_{DS} = V_{GS} = 0V, f = 1MHz$ | - | 2.5 | - | Ω |
| Dynamic characteristics | | | | | | |
| Input Capacitance | C_{iss} | $V_{DS} = 50V, V_{GS} = 0V, f = 1MHz$ | - | 4590 | - | pF |
| Output Capacitance | C_{oss} | | - | 1060 | - | |
| Reverse Transfer Capacitance | C_{rss} | | - | 38.4 | - | |
| Switching characteristics | | | | | | |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD} = 50V, I_D = 80A,$ $V_{GS} = 10V, R_G = 6\Omega$ | - | 20.4 | - | ns |
| Turn-on rise time | t_r | | - | 31 | - | |
| Turn-off delay time | $t_{d(off)}$ | | - | 76.8 | - | |
| Turn-off fall time | t_f | | - | 36.2 | - | |
| Total Gate Charge | Q_g | $V_{DS} = 50V, I_D = 40A,$ $V_{GS} = 10V$ | - | 79 | - | nC |
| Gate-Source Charge | Q_{gs} | | - | 16 | - | |
| Gate-Drain Charge | Q_{gd} | | - | 16.4 | - | |
| Reverse Recovery Charge | Q_{rr} | $I_F = 80A, di/dt = 100A/us$ | | 52.7 | | nC |
| Reverse Recovery Time | T_{rr} | $I_F = 80A, di/dt = 100A/us$ | | 43.4 | | ns |
| Source-Drain Diode characteristics | | | | | | |
| Diode Forward voltage ⁽³⁾ | V_{DS} | $V_{GS} = 0V, I_S = 40A$ | - | 0.85 | 1.3 | V |
| Diode Forward current ⁽⁴⁾ | I_S | | - | - | 85 | A |

Notes:

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. EAS Condition: $T_J = 25^\circ C, V_{DD} = 50V, R_G = 25\Omega, L = 0.5Mh, I_{AS} = 32A$
3. Pulse Test: pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
4. Surface Mounted on FR4 Board, $t \leq 10$ sec

■ Test circuits and waveforms

Figure A: Gate Charge Test Circuit & Waveforms

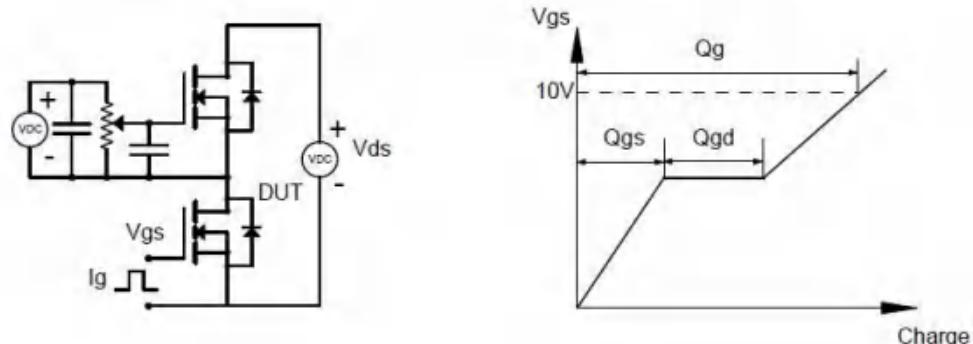


Figure B: Resistive Switching Test Circuit & Waveforms

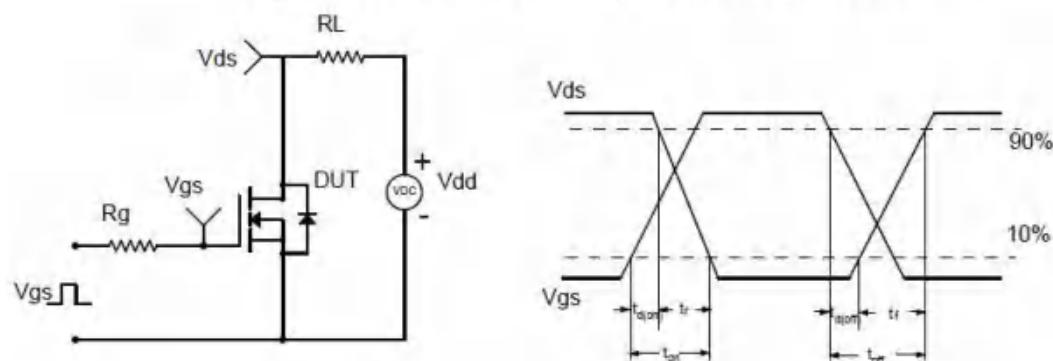


Figure C: Unclamped Inductive Switching (UIS) Test

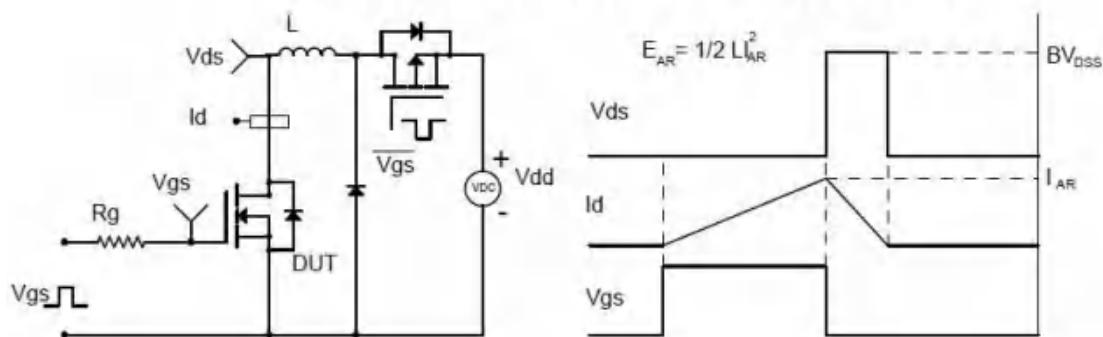
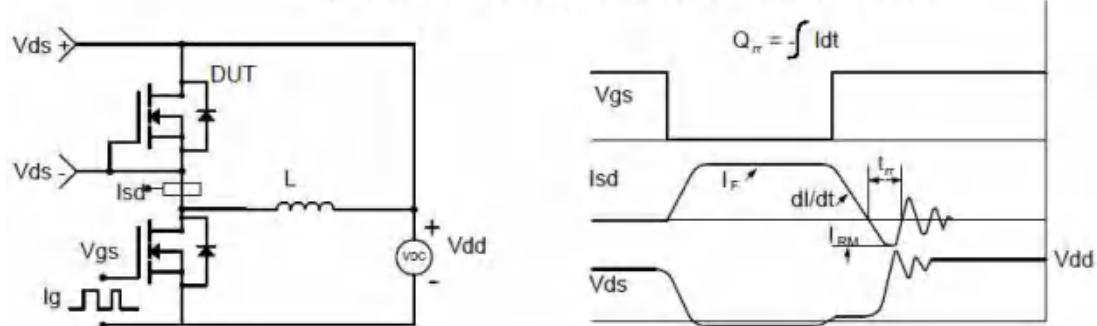


Figure D: Diode Recovery Test Circuit & Waveforms



Typical Electronic and Thermal Characteristics

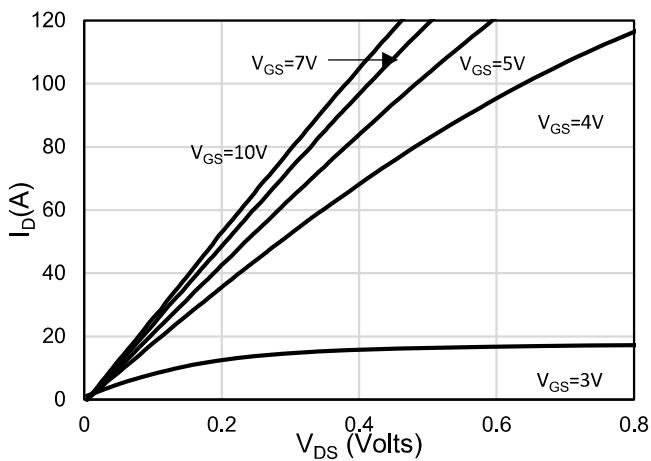


Figure 1: On-Region Characteristics

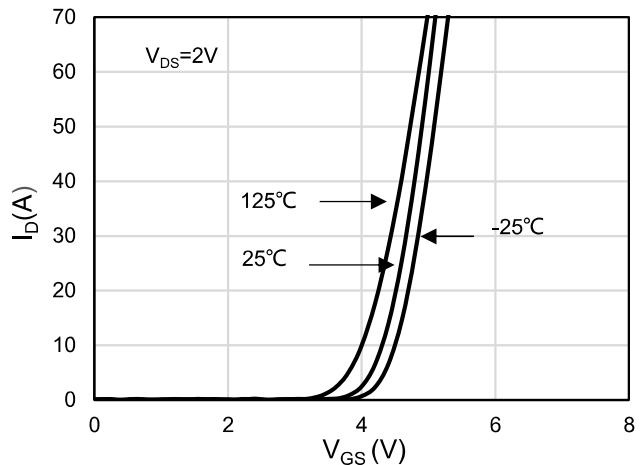


Figure 2: Transfer Characteristics

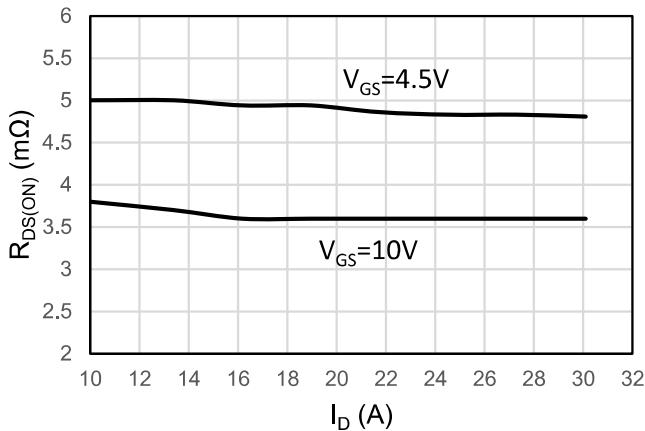


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

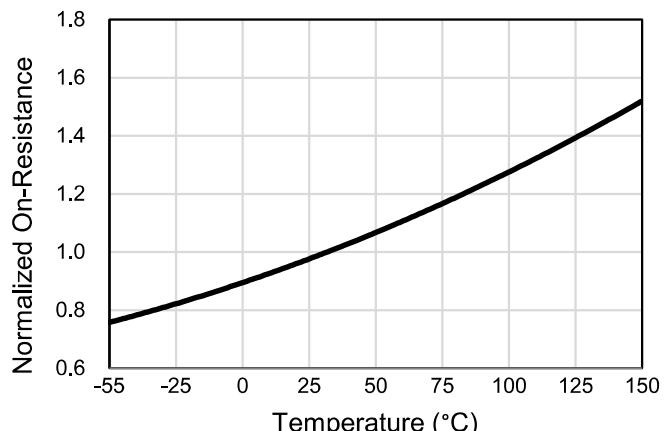


Figure 4: On-Resistance vs. Junction Temperature

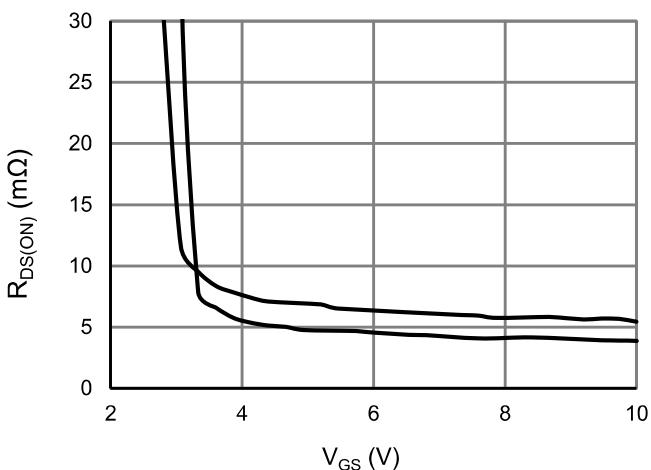


Figure 5: On-Resistance vs. Gate-Source Voltage

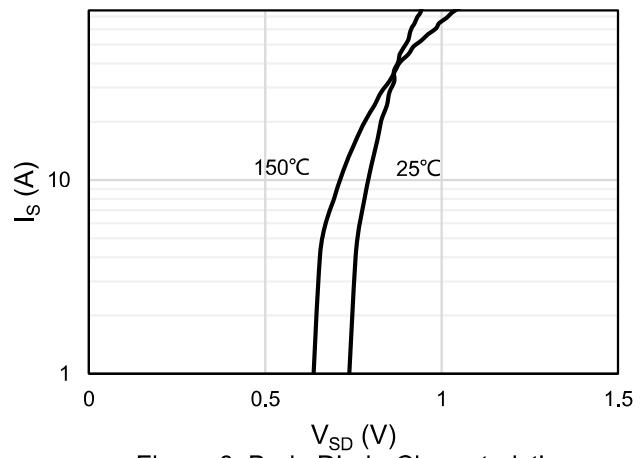


Figure 6: Body-Diode Characteristics

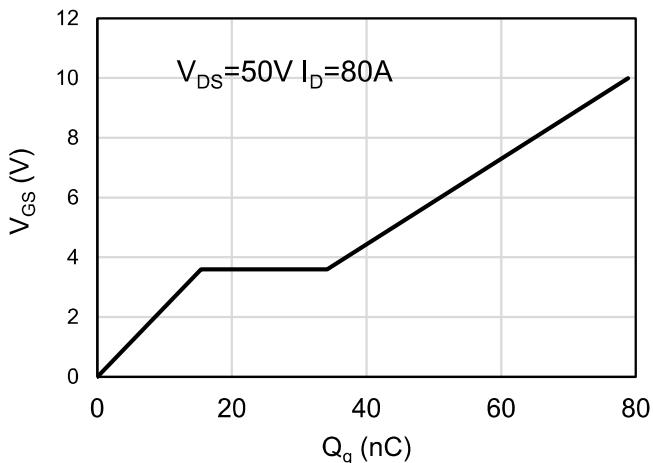


Figure 7: Gate-Charge Characteristics

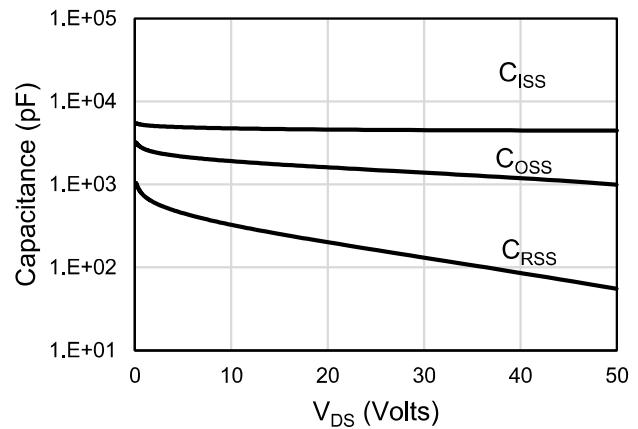


Figure 8: Capacitance Characteristics

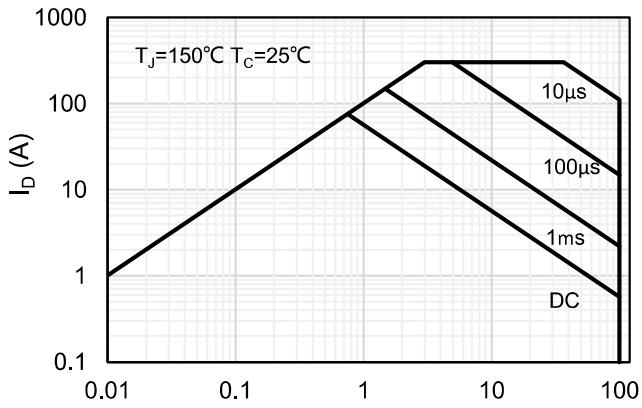


Figure 9: Maximum Forward Biased Safe Operating Area

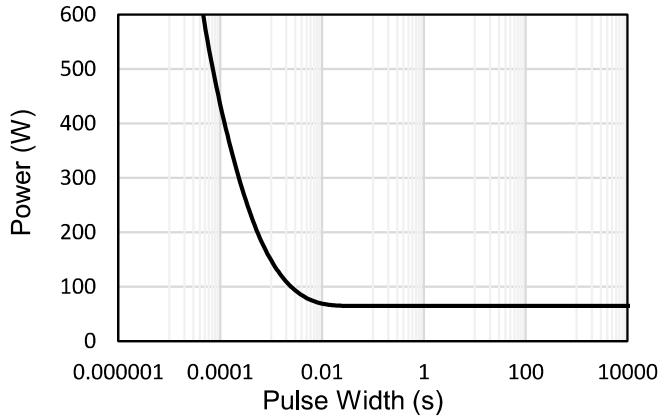


Figure 10: Single Pulse Power Rating Junction-to-Case

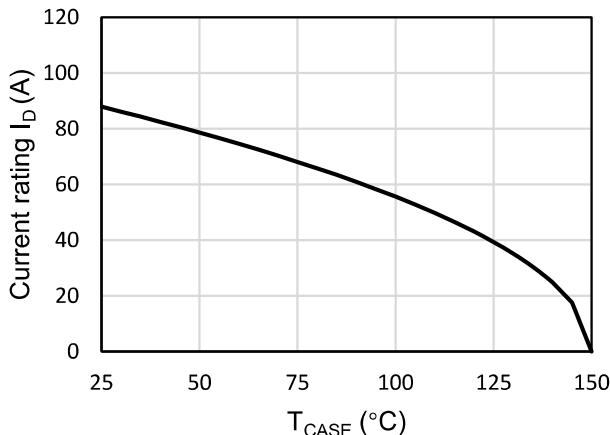


Figure 13: Current De-rating

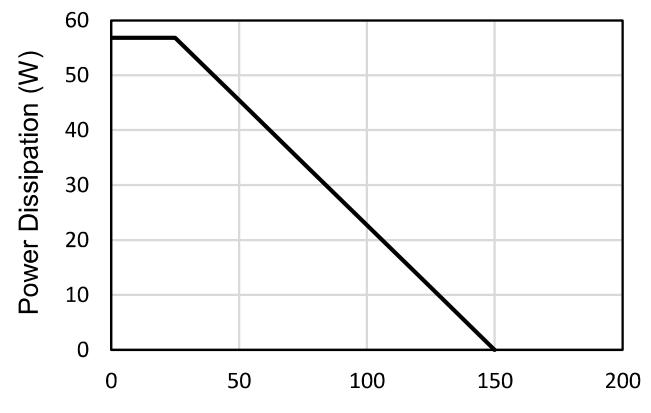


Figure 12: Power De-rating

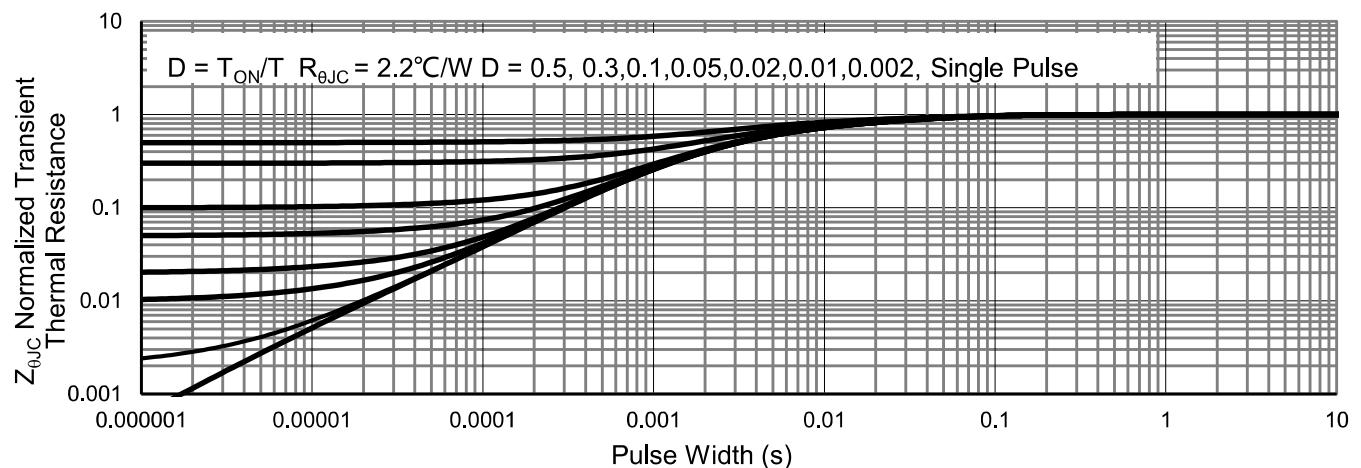
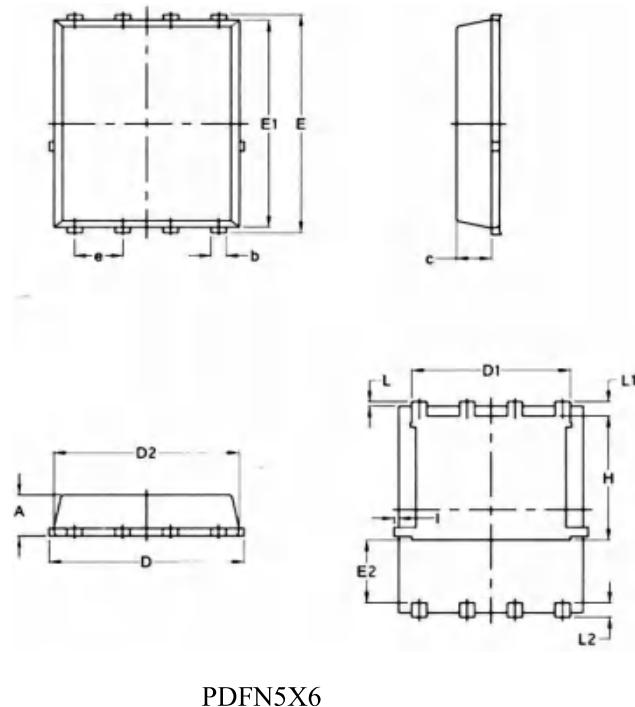


Figure 13: Normalized Maximum Transient Thermal Impedance

PDFN5X6 Package Information



PDFN5X6

| S Y M B O L | COMMON | | | |
|----------------------------|--------|-------|--------|--------|
| | MM | | INCH | |
| | MIN. | MAX. | MIN. | MAX. |
| A | 1.03 | 1.17 | 0.0406 | 0.0461 |
| b | 0.34 | 0.48 | 0.0134 | 0.0189 |
| c | 0.824 | 0.970 | 0.0324 | 0.0382 |
| D | 4.80 | 5.40 | 0.1890 | 0.2126 |
| D1 | 4.11 | 4.31 | 0.1618 | 0.1697 |
| D2 | 4.80 | 5.00 | 0.1890 | 0.1969 |
| E | 5.95 | 6.15 | 0.2343 | 0.2421 |
| E1 | 5.65 | 5.85 | 0.2224 | 0.2303 |
| E2 | 1.60 | — | 0.0630 | — |
| e | 1.27 | BSC | 0.05 | BSC |
| L | 0.05 | 0.25 | 0.0020 | 0.0098 |
| L1 | 0.38 | 0.50 | 0.0150 | 0.0197 |
| L2 | 0.38 | 0.50 | 0.0150 | 0.0197 |
| H | 3.30 | 3.50 | 0.1299 | 0.1378 |
| I | — | 0.18 | — | 0.0070 |